

ABSTRACT OF THE DISCLOSURE

In a pixel circuit constituting one pixel of the two-dimensional matrix light modulation device consisting of a plurality of pixels, a drive circuit (DR) for driving a light modulating layer (PM) is constituted by a ferroelectric gate FET (Tr). The pixel electrode (1) in each pixel circuit is connected to the drain of the ferroelectric gate FET (Tr), and voltage ( $V_{com}$ ) is applied to a counter electrode (2). The source electrodes and substrate electrodes of the matrix light modulation device of the same column are connected together, and a data signal ( $V_b$ ) is input in a column unit. Likewise, the gate electrodes of the same row are connected together, and a row selecting signal ( $V_g$ ) is input in a row unit.

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